

S/N 10/675,910

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ashik Kumar Shivacharya Nagaraj et al.

Examiner: Satish S. Rampuria

Serial No.: 10/675,910

Group Art Unit: 2191

Filed: September 30, 2003

Docket No.: 884.891US1

Title: COMBINATIONAL APPROACH FOR DEVELOPING BUILDING BLOCKS OF DSP
COMPILER

Customer Number: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

This responds to the Office Action mailed on April 4, 2007. Please amend the above-identified patent application as follows.